



Nagesh 10-26-12-10-40

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Patent Application**

Applicant(s): H.S. Nagesh et al.  
Case: 10-26-12-10-40  
Serial No.: 10/785,352  
Filing Date: February 24, 2004  
Group: 2661  
Examiner: To Be Assigned

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit: May 19, 2004

Signature: Risa L. Vulpis

Title: Load Balancing Method and Apparatus for  
Ethernet Over SONET and Other Types of Networks

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

In response to the Notice to File Missing Parts dated May 17, 2004, Applicants submit herewith eight (8) sheets of formal drawings for the above-referenced patent application.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,

Date: May 19, 2004

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